FACULTY PROFILE

Faculty Name: A. RAMESH

Faculty Photo:



Faculty Description:

A. Ramesh is the Assistant Professor, Department of Electrical and Electronics Engineering at Dr.K.V.Subba Reddy Institute of Technology, Kurnool.

Profile Tab:

Qualification

Qualification	Institution	Year
Bachelor's degree	G Pullaiah College of Engineering and Technology, Kurnool	2013
Master's degree	CHAITHANYA BHARATHI INSTITUTE OF TECHNOLOGY GANDIPET HYDERABAD.	2018

Experience

Designation	Institution	From	То
Assistant Professor	SRINIVASA RAMANUJAN INSTITUTE OF TECHNOLOGY, ANANTAPUR	2019	2020
Assistant Professor	Dr .K.V.Subba Reddy Institute of Technology, Kurnool.	2020	Till date

Responsibilities Tab:

Responsibilities

- 1. Placement Coordinator
- 2. Attendance report
- 3. Machine lab incharge
- 4. Mentor

Teaching Tab:

Expertise / list of subjects handled

Under Graduate Courses: Generation of Electrical Power, Power Quality, Electrical Distribution Systems, control systems, measurement and sensors.

Post Graduate Courses: FACTS

Research Tab:

Research Interest

- Intelligent Feeder Control System in 230KV Switchyard.
- Over Voltage and under Voltage Protection System

Publications tab:

Journals

- **1.** S.Masum Basha, P. Narendra, A. Ramesh, "MODELLING AND DESIGN OF MULTILEVEL CONVERTERS WITH SYMMETRICAL HALF-BRIDGE SUBMODULES AND SENSORLESS VOLTAGE BALANCE", in POSITIF Journal, Vol 22, Issue 9, 2022.
- 2. B. Prudvi kumar Reddy, K.Sivaramudu, A. Ramesh, "CONTROLLING THE CURRENT IN A SMALL-SCALE DC MICROGRID REQUIRES THE USE OF A MULTI-LEVEL CONVERTER", Journal of Nonlinear Analysis and Optimization: Theory & Applications, Vol. 13, Issue. 2, 2022.

FDPs AND STTPS TAB:

- **1.** Participated in the Training program/FDP on "FIRST HAND EXPERIENCE ON SPSS INTRODUCTRORY MODULE" Organised by faculty of commerce-Parul University, Research foundation of India & RFI care from 06 November to 12 November 2022
- **2.** Participated in the online Faculty Development Programme on "Green Energy Technologies for Sustainable Development" organized by EEE Department, SSET from 23/05/2022to27/05/2022

3. Participated and successfully completed the AICTE Sponsored Six day online Short Term Training Program (STTP) on Novel Design & Control Strategies and Innovative Technical Practices in LV/HV Modern Switchgear from 23.07.2020 to 29.07.2020 organised by the Department of Electrical and Electronics Engineering, Saranathan college of Engineering.

Blog Tab:

Blog link : <u>kummarimahesh.blogspot.com</u>